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M Beaumont - US Patent App. 10/689,280, 2003 - Google Patents

... For load balancing, there is a requirement to distribute T ... the processors with A tasks and with B tasks. ... generating a key comprised of the reverse bit order of ...

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P Rosch, K Sattler, C Weth, E Buchmann - ICDE Workshop NetDB, 2005 - doi.ieeecomputersociety.org

... 16] presents an outright description of reverse bit interleaving. ... Thus, an important task for future work ... to other peers, especially for load balancing purposes ...

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NT Slingerland, AJ Smith - Microprocessors and Microsystems, 2005 - Elsevier

... Memory 3.5.1. Load/store operations 3.5.1.1. ... Implementing a data path greater than 128-bits was not viewed as a reasonable option when balancing cost against ...

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S Kumar - 2003 - kava.student.usp.ac.fj

... load/ unload unit uses DC motors for movement ... interleaving path planning or the wave front based planners can be used to determine ... means to accomplish the task. ...

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